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APPENDIX [redacted]

490 I/O CONNECTOR (information) PIN ASSIGNMENTS

Bit	104 Pin	490 88 Color Code	Bit	104 Pin	490 88 Color Code
0	A	900	21	W	946
0	E	901	21	AA	947
1	B	902	22	x	948
1	F	903	22	AB	950
2	C	904	23	y	951
2	H	905	23	AC	952
3	D	906	24	AD	943
3	J	907	24	AJ	954
4	K	908	25	AE	955
4	P	910	25	AK	956
5	L	911	26	AF	957
5	R	912	26	AL	958
6	M	913	27	AH	960
6	S	914	27	AM	961
7	N	915	28	AN	962
7	T	916	28	AT	963
8	U	917	29	AP	964
8	Y	918	29	AU	965
9	V	920	30	AR	966
9	Z	921	30	AV	967
10	W	922	31	AS	968
10	a	923	31	AW	970
11	C	924	32	AX	971
11	b	925	32	BB	972
12	c	926	33	AY	973
12	h	927	33	BC	974

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APPENDIX II

[redacted] 490 I/O CONNECTOR (Information) PIN ASSIGNMENTS

Bit	104 Pin	490 88 Color Code	Bit	104 Pin	490 88 Color Code
0	A	900	21	W	946
0	E	901	21	AA	447
1	B	902	22	X	948
1	F	903	22	AB	950
2	C	904	23	Y	951
2	H	905	23	AC	952
3	D	906	24	AD	943
3	J	907	24	AJ	954
4	K	908	25	AE	955
4	P	910	25	AK	956
5	L	911	26	AF	957
5	R	912	26	AL	958
6	M	913	27	AH	960
6	S	914	27	AM	961
7	N	915	28	AN	962
7	T	916	28	AT	963
8	U	917	29	AP	964
8	Y	918	29	AU	965
9	V	920	30	AR	966
9	Z	921	30	AV	967
10	W	922	31	AS	968
10	a	923	31	AW	970
11	C	924	32	AX	971
11	b	925	32	BB	972
12	c	926	33	AY	973
12	h	927	33	BC	974

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Bit	Pin	88 Color Code
13	d	928
13	d	930
14	f	931
14	j	932
15	g	933
15	k	934
16	m	935
16	r	936
17	n	937
17	s	938
18	p	940
18	t	941
19	q	942
19	u	943
20	v	944
20	z	945

Bit	Pin	88 Color Code
34	AZ	975
34	BD	976
35	BA	977
35	BE	978

First pin each pair carries the SIGNAL.

Second pin is the ground or return.

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Bit	104 Pin	490 88 Color Code	Bit	104 Pin	490 88 Color Code
13	d	928	34	AZ	975
13	d	930	34	BD	976
14	f	931	35	BA	977
14	j	932	35	BE	978
15	g	933			
15	k	934			
16	m	935			
16	r	936			
17	n	937			
17	s	938			
18	p	940			
18	t	941			
19	q	942			
19	u	943			
20	v	944			
20	z	945			

First pin each pair carries the SIGNAL.

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[redacted] 490 I/O CONNECTORS (Information) PIN ASSIGNMENTS

First Pin each pair carries the SIGNAL.

Second pin is the ground or return.

Input	Output	104 Pin	490 Color Code
Input Data Request	Output Acknowledge	BU	982
Input Data Request	Output Acknowledge	BY	983
External Interrupt	External Function	CJ	1
External Interrupt	(Chan 2-13)	CN	2
Input Acknowledge	Output Data Request	BT	980
Input Acknowledge	Output Data Request	BX	981
Input Buffer Active	Output Buffer Active	BH	92
(Chan 0-1)	(Chan 0-1)	BM	98
Ex'ernally Specified	Scanner Request	CK	3
Add. (Chan 2-13 only)	Scn (CCU to S/SO)	CP	5
Ex'ernally Specified	Scanner Select	BJ	984
Index (Chan 2-13 only)	(s/s to CCU)	BN	985
Hang Up (Uniset Equip. Comples to CCU) of		CL	90
(with Chan Wyn.) as bootstrap Error Input		CR	91
MASTER CLEAR (Chan. Synchronizer)		CA	986
MASTER CLEARA(Chan. Synchronizer)		CE	987

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[redacted] 490 I/O CONNECTORS (Information) PIN ASSIGNMENTS

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Input	Output	104 Pin	490 Color Code
Input Data Request	Output Acknowledge	BU	982
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